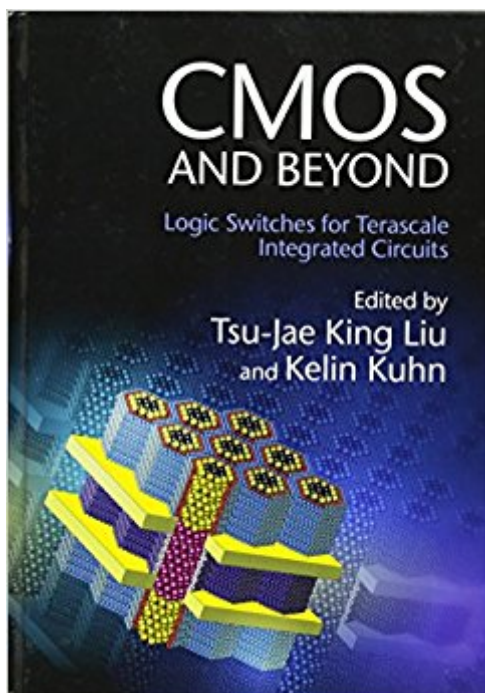


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# CMOS And Beyond: Logic Switches For Terascale Integrated Circuits



## Synopsis

Get up to speed with the future of logic switch design with this indispensable overview of the most promising successors to modern CMOS transistors. Learn how to overcome existing design challenges using novel device concepts, presented using an in-depth, accessible, tutorial-style approach. Drawing on the expertise of leading researchers from both industry and academia, and including insightful contributions from the developers of many of these alternative logic devices, new concepts are introduced and discussed from a range of different viewpoints, covering all the necessary theoretical background and developmental context. Covering cutting-edge developments with the potential to overcome existing limitations on transistor performance, such as tunneling field-effect transistors (TFETs), alternative charge-based devices, spin-based devices, and more exotic approaches, this is essential reading for academic researchers, professional engineers, and graduate students working with semiconductor devices and technology.

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## Customer Reviews

Learn how to overcome existing logic switch design challenges with this in-depth, accessible, tutorial-style overview of the most promising successors to modern CMOS technology, written by leading researchers from industry and academia. Essential reading for academic researchers, professional engineers, and graduate students within the semiconductor community.

Tsu-Jae King Liu is the Conexant Systems Distinguished Professor, Department of Electrical Engineering and Computer Sciences at the University of California, Berkeley. A co-inventor of the FinFET, she has been awarded the IEEE Kiyo Tomiyasu Award (2010), the Electrochemical Society Thomas D. Calinan Award (2011), and the Intel Outstanding Researcher Award (2012). She is a Fellow of the IEEE. Kelin Kuhn is an Intel Fellow and Director of Advanced Device Technology in the Technology and Manufacturing Group at Intel Corporation. She is responsible for research on new device architectures and has been awarded two Intel Achievement Awards, one for high-k metal-gate (2006) and one for Trigate (2008), as well as the IEEE Paul Rappaport Award (2013). She is a Fellow of the IEEE.

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